

STTH6006W

Turbo 2 ultrafast - high voltage rectifier

Table 1. Main product characteristics				
I _{F(AV)}	60 A			
V _{RRM}	600 V			
Tj	175° C			
V _F (typ)	1.1 V			
t _{rr} (max)	60 ns			

Table 1. Main product characteristics

Features and benefits

- Ultrafast switching
- Low reverse current
- Low thermal resistance
- Reduces conduction and switching losses

Description

The STTH6006W uses ST Turbo 2 600 V technology. This device is specially suited for use in switching power supplies, and industrial applications. The V_F / T_{rr} trade-off has been specially established to increase the performance in welding applications.

А — ▶ — К
K
DO-247
STTH6006W

Table 2.Order code

Part number	Marking
STTH6006W	STTH6006W

Table 3. Absolute ratings (limiting values per diode at 25° C, unless otherwise specified)

Symbol	Parameter			Unit
V _{RRM}	Repetitive peak reverse voltage		600	V
I _{F(RMS)}	RMS forward current		90	А
I _{F(AV)}	Average forward current, $\delta = 0.5$	$T_c = 95^\circ C$	60	А
I _{FSM}	Surge non repetitive forward current	t _p = 10 ms Sinusoidal	400	А
T _{stg}	Storage temperature range		-65 to + 175	°C
Тj	Maximum operating junction tempera	175	°C	

 $1. \quad \frac{dP_{tot}}{dT_j} < \frac{1}{R_{th(j-a)}} \quad \text{to avoid thermal runaway for a diode on its own heatsink}$

1 Characteristics

Table 4. Thermal parameters

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case	0.75	°C/W

Table 5. Static electrical characteristics

Symbol	Parameter	Test conditions		Min.	Тур	Max.	Unit
I _B ⁽¹⁾	Reverse leakage current	$T_j = 25^\circ C$	V – V			50	μA
'R` ′	R ^V neverse leakage current	T _j = 125° C	$V_{R} = V_{RRM}$		160	1600	μΑ
V (2)	V _F ⁽²⁾ Forward voltage drop	T _j = 25° C	I _F = 60 A			1.85	v
VF`		T _j = 150° C	F = 00 A		1.10	1.40	v

1. Pulse test: $t_p = 5 \text{ ms}, \delta < 2 \%$

2. Pulse test: $t_p = 380 \ \mu s, \ \delta < 2 \ \%$

To evaluate the conduction losses use the following equation: P = 1.07 x $I_{F(AV)}$ + 0.006 ${I_F}^2_{(RMS)}$

Table 6.Dynamic characteristics

Symbol	Parameter	Test conditions	Min.	Тур	Max.	Unit
+	Reverse recovery time	$I_F = 0.5 \text{ A}, I_{rr} = 0.25 \text{ A}, I_R = 1 \text{ A}, T_j = 25^{\circ} \text{ C}$			60	ns
۲rr	t _{rr} Reverse recovery time	$I_F = 1 \text{ A, } dI_F/dt = -50 \text{ A/}\mu\text{s},$ $V_R = 30 \text{ V, } T_j = 25^{\circ} \text{ C}$		60	85	115
I _{RM}	Reverse recovery current	$I_F = 60 \text{ A}, \text{ dI}_F/\text{dt} = -100 \text{ A}/\mu\text{s}, V_R = 400 \text{ V}, T_j = 150^{\circ} \text{ C}$		10.5	14	
t _{fr}	Forward recovery time	$\begin{array}{l} I_F = 60 \ A & dI_F/dt = 200 \ A/\mu s \\ V_{FR} = 1.1 \ x \ V_{Fmax}, \ T_j = 25^\circ \ C \end{array}$			500	ns
V _{FP}	Forward recovery voltage	$\begin{split} I_F &= 60 \text{ A} \qquad dI_F/dt = 200 \text{ A}/\mu\text{s} \\ V_{FR} &= 1.1 \text{ x} \text{ V}_{Fmax}\text{, } \text{T}_j = 25^\circ \text{ C} \end{split}$		3		V



Figure 1. Conduction losses versus average current

Figure 2. Forward voltage drop versus forward current

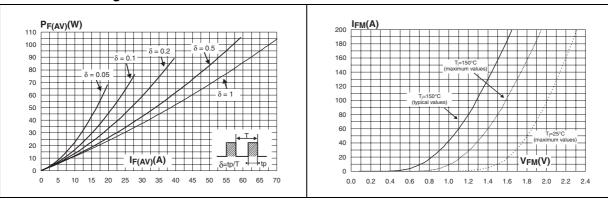


Figure 3. Relative variation of thermal impedance junction to case versus pulse duration

Peak reverse recovery current versus dl_F/dt (typical values)

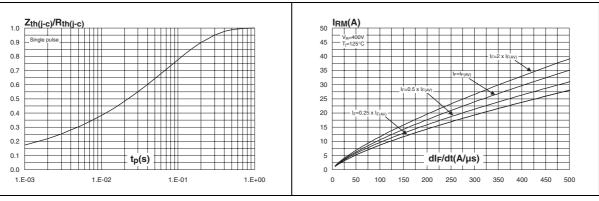
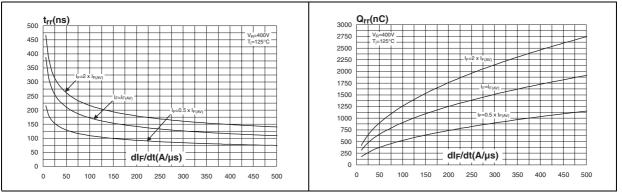


Figure 4.

Figure 5. Reverse recovery time versus dl_F/dt (typical values)

Figure 6. Reverse recovery charges versus dl_F/dt (typical values)



57

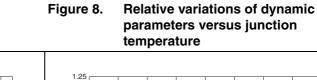
IF=IF(AV) VR=400V

100

Ti=125°C

125

Figure 7. Softness factor versus dl_F/dt (typical values)



I_{BM}

50

ORF

Tj(°C)

75

1.00

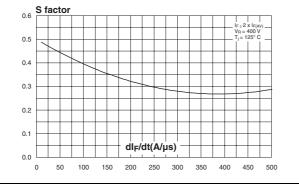
0.75

0.50

0.25

0.00

25



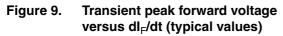


Figure 10. Forward recovery time versus dl_F/dt (typical values)

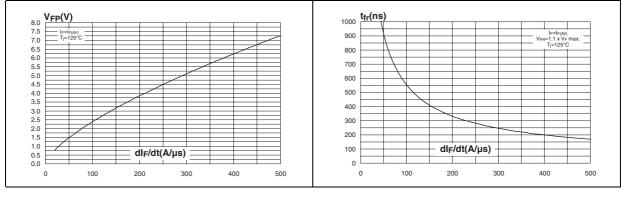
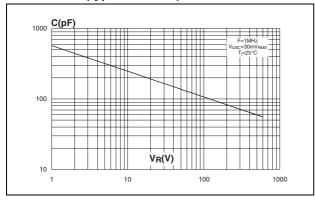


Figure 11. Junction capacitance versus reverse voltage applied (typical values)

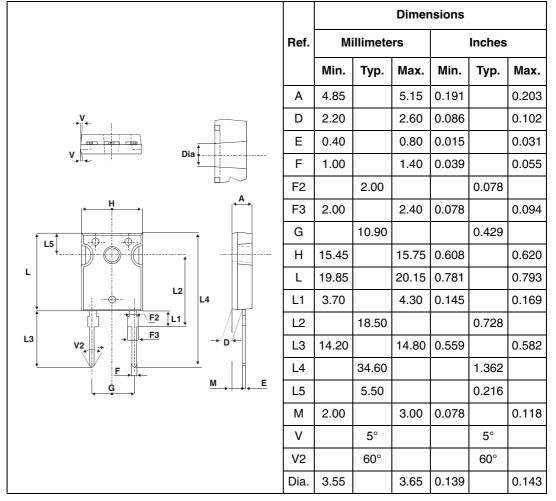




2 Package mechanical data

- Epoxy meets UL94, V0
- Cooling method: by conduction (C)
- Recommended torque value: 0.80 Nm
- Maximum torque value: 1.0 Nm

Table 7.DO-247 dimensions



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.



3 Ordering information

Table 8.Ordering information

Part Number	Marking	Package	Weight	Base qty	Delivery mode
STTH6006W	STTH6006W	DO-247	4.40 g	30	Tube

4 Revision history

Table 9. Revision history

Date	Revision	Changes
18-May-2006	1	First issue.
11-Jul-2007	2	Reformatted to current standards. Updated Table 7.

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